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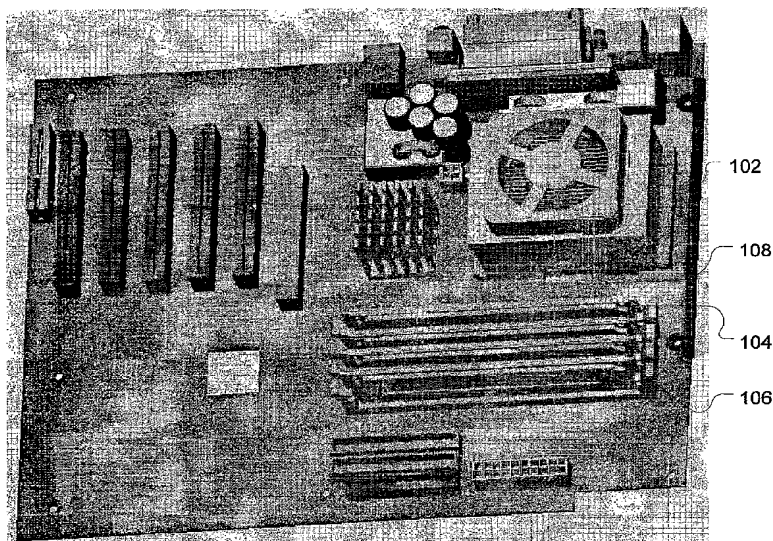
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(54) Title: A PACKAGE INCLUDING A MICROPROCESSOR AND FOURTH LEVEL CACHE

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(57) Abstract: A method, apparatus and system with a package including an integrated circuit disposed between die including a microprocessor and a die including a fourth level cache.

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